

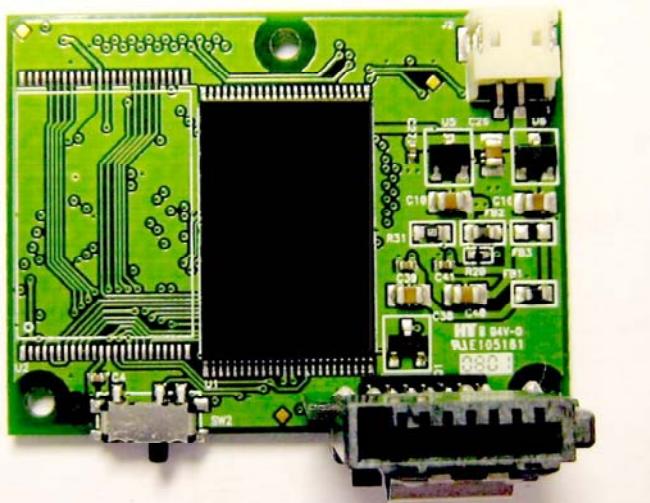
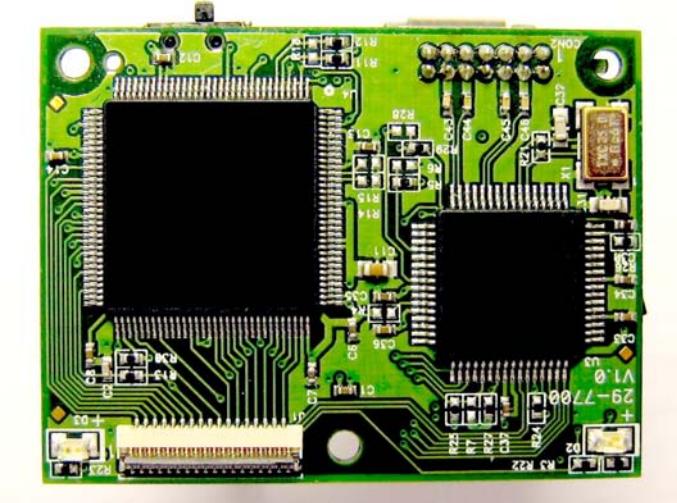
# **TS512M/1G/2GS DOM7H**

**7P Horizontal Type  
SATA Flash Modules**

## Description

Serial ATA is designed as a successor to the legacy SATA standard. The new standard is dedicated to achieve higher transfer rate with thinner cables and more reliable operation than Parallel ATA interface. SATA Flash Modules is perfect replacement storage device for PCs, Laptops, gaming systems, and handheld devices.

## Placement



## Features

- RoHS compliant
- Fully compatible with devices and OS that support the SATA 1.0a 1.5Gbps standard
- Non-volatile Flash Memory for outstanding data retention
- Supports up to Ultra DMA Mode 4
- Built-in ECC (Error Correction Code) functionality and wear-leveling algorithm ensures highly reliable data transfer
- Mechanical write-protect switch
- 7 pin female SATA connector with latch
- Connect to motherboard directly without cable
- Low Power Consumption
- 3.3V / 5V Power Supply.
- Shock resistance

## Dimensions

Side	Millimeters	Inches
A	40.0 ± 0.3	1.57 ± 0.012
B	30.0 ± 0.2	1.18 ± 0.008
C*	24.3 ± 0.5	0.96 ± 0.020

\* Please refer to Mechanical Drawing

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## Specifications

<b>Physical Specification</b>		
<b>Form Factor</b>		SATA Flash Module Horizontal Type
<b>Storage Capacities</b>		512M~2GB (T.B.D)
<b>Dimensions (mm)</b>	<b>Length</b>	40.0 ± 0.3
	<b>Width</b>	30.0 ± 0.2 (Not including Write-Protect Switch and SATA connector)
	<b>Height</b>	24.3 ± 0.5 (refer to mechanical drawing)
<b>Input Voltage</b>		5V ± 10% / 3.3V ± 5%
<b>Weight</b>		8 g
<b>Connector</b>		Signal: SATA 7 pins female connector with latch Power: 2P 1.5mm pitch wafer connector

<b>Environmental Specifications</b>	
<b>Operating Temperature</b>	0 °C to 70 °C
<b>Storage Temperature</b>	- 40 °C to 85 °C

<b>Power Consumption</b>		
<b>2GB SATA Flash Modules</b> <b>Power Consumption</b> (DC 5V @25°C)		<b>Crystal Disk Mark 1.0.6 / 50MB</b>
	<b>Read</b>	164 mA(average) / 200 mA(peak)
	<b>Write</b>	170 mA(average) / 210 mA(peak)
	<b>Standby</b>	129.4 mA

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<b>Reliability</b>	
<b>Data Reliability</b>	Built-in 4 symbol/page correction ECC
<b>Data Retention</b>	10 years
<b>Connector Durability</b>	500 mating cycles

<b>Performance*</b>				
<b>Model P/N</b>	<b>Read (KB/s)</b>	<b>Write (KB/s)</b>	<b>Random Read (KB/s)</b>	<b>Random Write (KB/s)</b>
<b>2GB SATA Flash Module</b>	25491	13260	24574	4085

\* Note : The performance is based on Samsung K9KAG08U0M, single channel. Environment: at 25 °C, tested with GA-8IG1000MK, 256 MB RAM, SATA interface support UDMA4, Windows® XP Version 2002 SP2, benchmark utility : HDBENCH (version 3.4006), copied file 100MB

<b>Actual Capacity</b>					
<b>Model P/N</b>	<b>Capacity</b>	<b>C/H/S</b>	<b>Capacity (BIOS)</b>	<b>DOS Format (Bytes)</b>	<b>Windows Format (Bytes)</b>
<b>1GB SATA Flash Module</b>	1GB	1942 / 16 / 63	1001MB	1,001,439,232	1,001,439,232
<b>2GB SATA Flash Module</b>	2GB	3884 / 16 / 63	2004MB	2,003,697,664	2,003,697,664

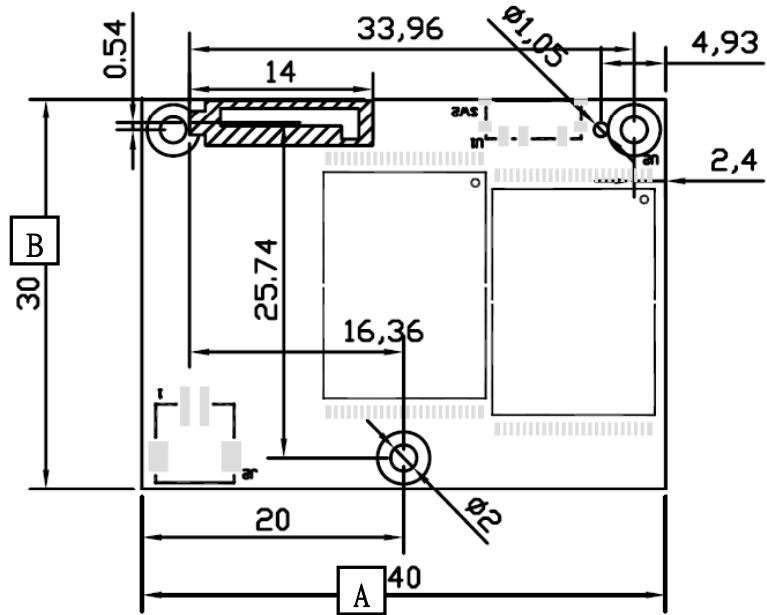
\* Note: FAT format

<b>Certification</b>	
<b>EMC</b>	CE, FCC and BSMI (T.B.D)

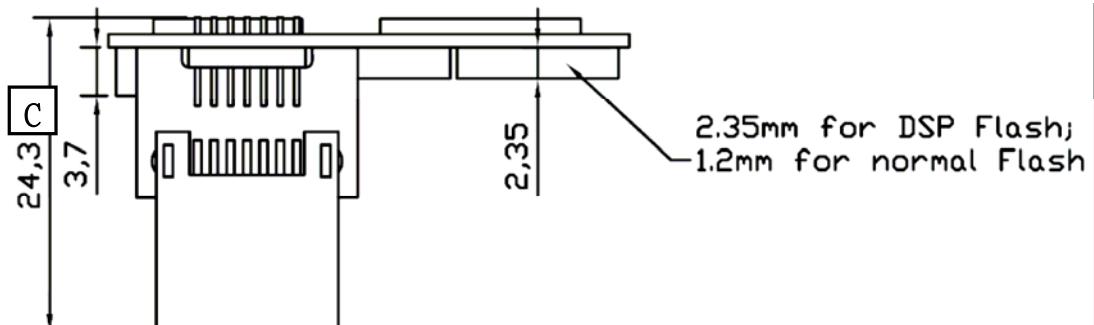
# TS512M/1G/2GS DOM7H

7P Horizontal Type  
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Mechanical Drawing



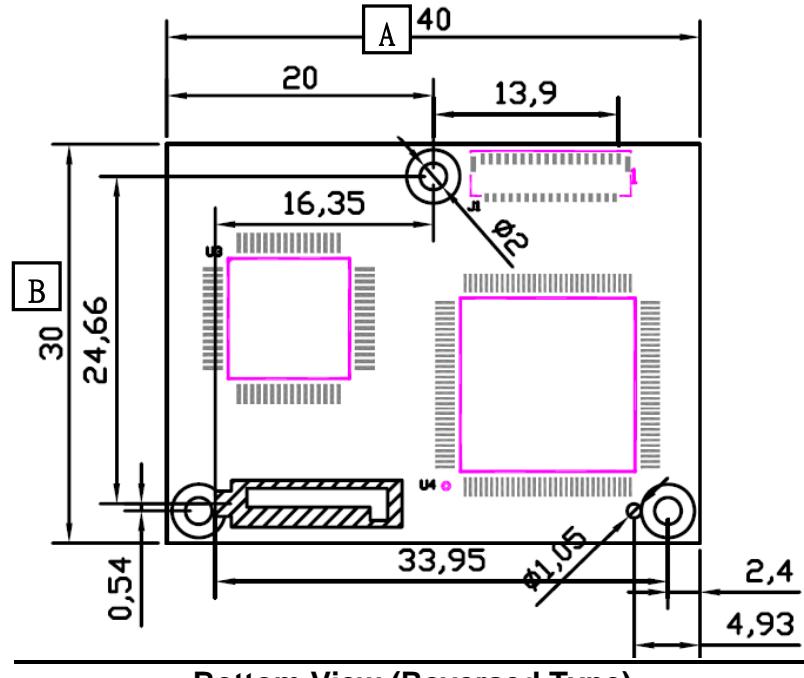
Bottom View (Normal Type)



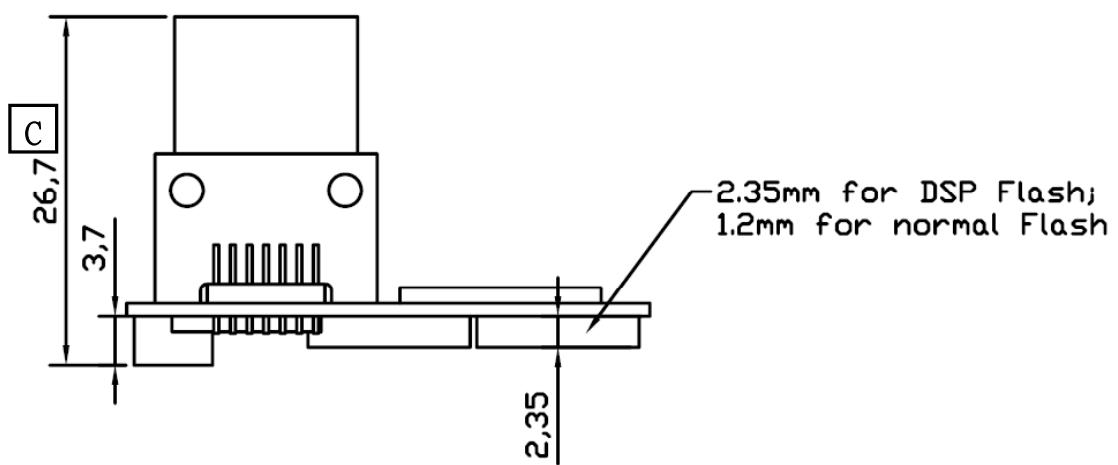
Side View (Normal Type)

# TS512M/1G/2GS DOM7H

7P Horizontal Type  
SATA Flash Modules



Bottom View (Reversed Type)



Side View (Reversed Type)

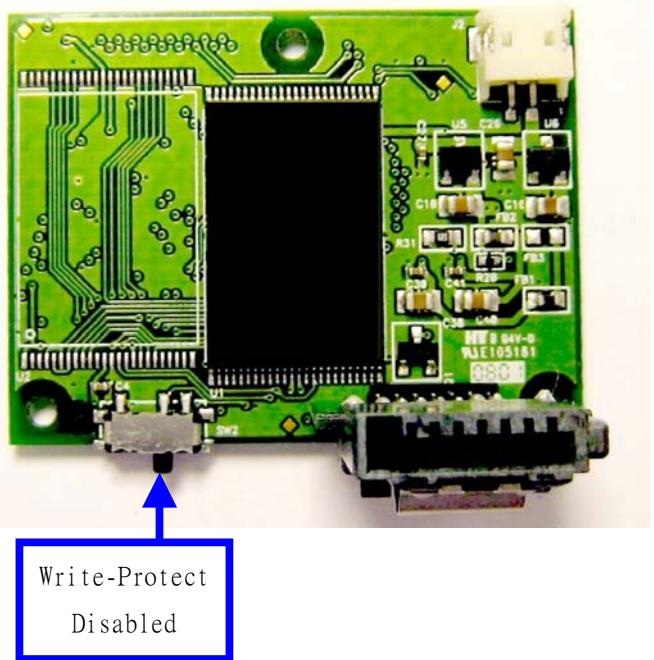
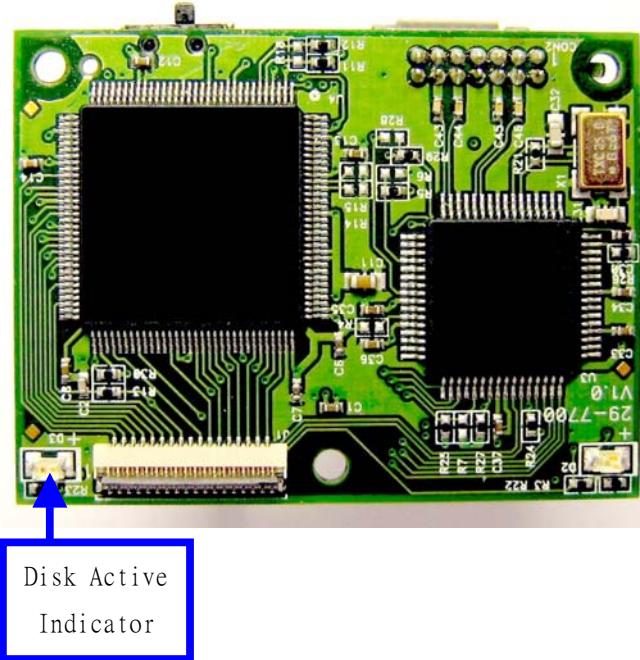
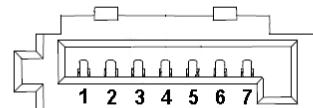
# **TS512M/1G/2GS DOM7H**

**7P Horizontal Type  
SATA Flash Modules**

## Functionality

### Pin Assignments

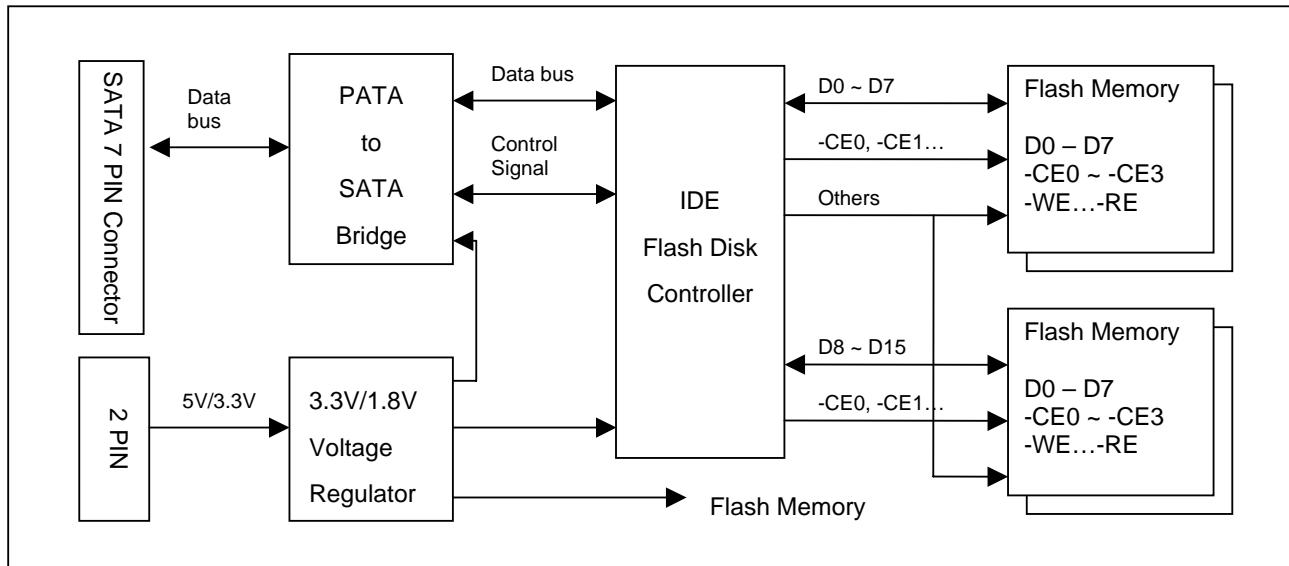
Pin No.	Pin Name
01	GND
02	A+
03	A-
04	GND
05	B-
06	B+
07	GND



# TS512M/1G/2GS DOM7H

7P Horizontal Type  
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## Block Diagram



## DC Characteristics

Parameter	Symbol	Min.	Max.	Unit	Remark
Supply Voltage	$V_{CC}$	2.97	5.5	V	
High level output voltage	$V_{OH}$	$V_{CC} - 0.8$		V	
Low level output voltage	$V_{OL}$		0.8	V	
High level input voltage	$V_{IH}$	2.4		V	Non-schmitt trigger
		2.05		V	Schmitt trigger <sup>1</sup>
Low level input voltage	$V_{IL}$		0.6	V	Non-schmitt trigger
			1.25	V	Schmitt trigger <sup>1</sup>
Pull up resistance <sup>2</sup>	$R_{PU}$	52.7	141	KOhm	
Pull down resistance	$R_{PD}$	47.5	172	kOhm	

1. Include CE1, CE2, HREG, HOE, HIOE, HWE, HIOW pins.

2. Include CE1, CE2, HREG, HOE, HIOE, HWE, HIOW, CSEL(P35), PDIAG, DASP pins.

**Command Set**

	<b>Command</b>	<b>Code</b>	<b>FR</b>	<b>SC</b>	<b>SN</b>	<b>CY</b>	<b>DH</b>	<b>LBA</b>	<b>Status</b>	<b>Note</b>
1	Check Power Mode	E5 or 98h	—	—	—	—	Y	—	Support	
2	Execute Drive Diagnostic	90h	—	—	—	—	Y	—	Support	
3	<b>Erase Sector</b>	<b>C0h</b>	—	Y	Y	Y	Y	Y	Not Support	#3
4	Flush Cache	E7h	—	—	—	—	Y	—	Support	
5	Format Track	50h	—	Y	—	Y	Y	Y	Support	
6	Identify Device	ECh	—	—	—	—	Y	—	Support	
7	Idle	E3h or 97h	—	Y	—	—	Y	—	Support	
8	Idle Immediate	E1h or 95h	—	—	—	—	Y	—	Support	
9	Initialize Drive Parameters	91h	—	Y	—	—	Y	—	Support	
10	Key Management Structure Read	B9 (Feature 0-127)	Y	Y	Y	Y	Y	—	NOT Support	#1
11	Key Management Read Keying Material	B9 (Feature 80)	Y	Y	Y	Y	Y	—	NOT Support	#1
12	Key Management Change Key Management Value	B9 (Feature 81)	Y	Y	Y	Y	Y	—	NOT Support	#1
13	NOP	00h	—	—	—	—	Y	—	NOT Support	
14	Read Buffer	E4h	—	—	—	—	Y	—	Support	
15	Read DMA	C8h	—	Y	Y	Y	Y	Y	Support	
16	Read Long Sector	22h or 23h	—		Y	Y	Y	Y	NOT Support	#2
17	Read Multiple	C4h	—	Y	Y	Y	Y	Y	Support	
18	Read Sector(s)	20h or 21h	—	Y	Y	Y	Y	Y	Support	
19	Read Verify Sector(s)	40h or 41h	—	Y	Y	Y	Y	Y	Support	
20	Recalibrate	1Xh	—	—	—	—	Y	—	Support	
21	<b>Request Sense</b>	<b>03h</b>	—	—	—	—	Y	—	Not Support	#3
22	Security Disable Password	F6h	—	—	—	—	Y	—	Support	
23	Security Erase Prepare	F3h	—	—	—	—	Y	—	Support	
24	Security Erase Unit	F4h	—	—	—	—	Y	—	Support	
25	Security Freeze Lock	F5h	—	—	—	—	Y	—	Support	
26	Security Set Password	F1h	—	—	—	—	Y	—	Support	

27	Security Unlock	F2h	–	–	–	–	Y	–	Support	
28	Seek	7Xh	–	–	Y	Y	Y	Y	Support	
29	Set Feature	EFh	Y	–	–	–	Y	–	Support	
30	Set Multiple Mode	C6h	–	Y	–	–	Y	–	Support	
31	Set Sleep Mode	E6h or 99h	–	–	–	–	Y	–	Support	
32	Standby	E2 or 96h	–	–	–	–	Y	–	Support	
33	Standby Immediate	E0 or 94h	–	–	–	–	Y	–	Support	
34	Translate Sector	87h	–	Y	Y	Y	Y	Y	Not Support	#3
35	Wear Level	F5h	–	–	–	–	Y	–	Support	#4
36	Write Buffer	E8h	–	–	–	–	Y	–	Support	
37	Write DMA	CAh	–	Y	Y	Y	Y	Y	Support	
38	Write Long Sector	32h or 33h	–	–	Y	Y	Y	Y	Not Support	#2
39	Write Multiple	C5h	–	Y	Y	Y	Y	Y	Support	
40	Write Multiple w/o Erase	CDh	–	Y	Y	Y	Y	Y	Not Support	#3
41	Write Sector(s)	30h or 31h	–	Y	Y	Y	Y	Y	Support	
42	Write Sector(s) w/o Erase	38h	–	Y	Y	Y	Y	Y	Not Support	#3
43	Write Verify	3Ch	–	Y	Y	Y	Y	Y	Support	

#1: This command is optional, depending on the key Management scheme in use.

#2: Use of this command is not recommended.

#3: CFA feature set command is not supported by P-ATA to S-ATA bridge controller.

#4: If Security command 22~27 are supported, this command is not supported.

**Definitions**

FR = Features Register

SC =Sector Count register (00H to FFH, 00H means 256 sectors)

SN = Sector Number register

CY = Cylinder Low/High register

DH = Head No. (0 to 15) of Drive/Head register

LBA = Logic Block Address Mode Support

– = Not used for the command

Y = Used for the command

**SMART Command Set**

## ● SMART Command Set

SMART Feature Register Values			
D0h	Read Data	D4h	Execute OFF-LINE Immediate
D1h	Read Attribute Threshold	D8h	Enable SMART Operations
D2h	Enable/Disable Autosave	D9h	Disable SMART Operations
D3h	Save Attribute Values	DAh	Return Status

1. If reserved size is below the Threshold, the status can be read from Cylinder register by Return Status command (DAh).

## ● SMART Data Structure

BYTE	F / V	Description
0-1	X	Revision code
2-361	X	Vendor specific
362	V	Off line data collection status
363	X	Self-test execution status byte
364-365	V	Total time in seconds to complete off-line data collection activity
366	X	Vendor specific
367	F	Off-line data collection capability
368-369	F	SMART capability
370	F	Error logging capability 7-1 Reserved 0 1=Device error logging supported
371	X	Vendor specific
372	F	Short self-test routine recommended polling time (in minutes)
373	F	Extended self-test routine recommended polling time (in minutes)
374	F	Conveyance self-test routine recommended polling time (in minutes)
375-385	R	Reserved
<b>386-395</b>	<b>F</b>	<b>Date Code</b>
<b>396</b>	<b>V</b>	<b>Number of MU in device (0~n)</b>
<b>397+(n*6)</b>	<b>V</b>	<b>MU number</b>
<b>398+(n*6)</b>	<b>V</b>	<b>MU data block</b>

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<b>400+(n*6)</b>	<b>V</b>	<b>MU spare block</b>
<b>401+(n*6)</b>	<b>V</b>	<b>Init. Bad block</b>
<b>402+(n*6)</b>	<b>V</b>	<b>Last Defect Bad block ( Newest state)</b>
511	V	Data structure checksum

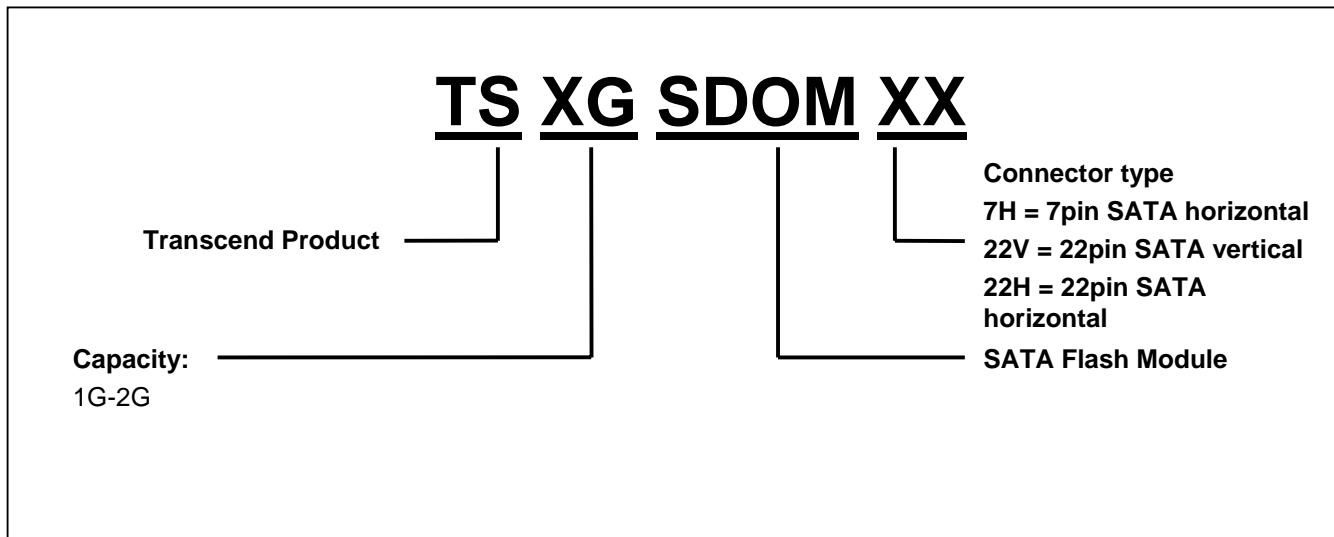
F=the content of the byte is fixed and does not change.  
V=the content of the byte is variable and may change depending on the state of the device or the commands executed by the device.  
X=the content of the byte is vendor specific and may be fixed or variable.  
R=the content of the byte is reserved and shall be zero.

\* 4 Byte value : [MSB] [2] [1] [LSB]

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## **Ordering Information**



1. The above technical information is based on industry standard data and has been tested to be reliable. However, Transcend makes no warranty, either expressed or implied, as to its accuracy and assumes no liability in connection with the use of this product. Transcend reserves the right to make changes to the specifications at any time without prior notice.

2. For specific capacity, performance, and reliability requirement, please contact with sales.

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*Exceeding Your Expectations*

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